

**Amendments to the Specification:**

*On page 5, after line 20, please add the following:*

Fig. 3 illustrates an example block diagram providing a general overview of an ASIC.

Fig. 4 illustrates an example of a wireless port embodiment.

Fig. 5 illustrates an example of a wireless port embodiment.

Fig. 6 illustrates an example of a wireless memory mapped port embodiment.